

ABSTRACT OF THE DISCLOSURE**METHOD AND APPARATUS FOR VERIFYING HARDWARE
IMPLEMENTATION OF A PROCESSOR ARCHITECTURE IN A LOGICALLY
PARTITIONED DATA PROCESSING SYSTEM**

- 5 A method, apparatus, and computer instructions for
testing hardware in a data processing system having
multiple partitions. A monitor process in a first
partition assigned to a first processor is initialized. A
random code generation process in a second partition
10 associated with a second processor is initialized. The
random code generation process generates instructions and
executes the instructions to test the second processor.
The monitor process monitors the random code generation
process and resets the second processor if the random
15 code generation process fails.

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